

# High Frequency Ceramic Solutions

**Sub-GHz Impedance Matched Balun + LPF integrated Passive Component for Texas Instruments' CC1310, CC1312 Chipsets**

**P/N: 0850BM14E0016**

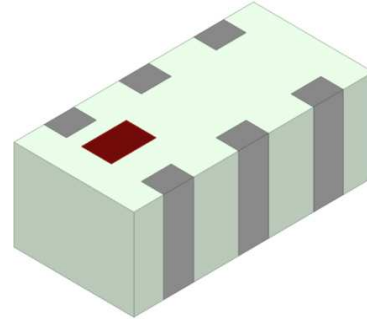
Detail Specification: 9/9/2021

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Do you need a small 900MHz antenna? Go to: <https://www.johansontechnology.com/antennas>

## General Specifications

|   |  |                     |
|---|--|---------------------|
| <b>Part Number</b>                                | 0850BM14E0016  |                     |
| <b>Frequency (MHz)</b>                            | 770 - 860  | 860 - 928           |
| <b>Unbalanced Impedance (<math>\Omega</math>)</b> | 50   |                     |
| <b>Balanced Impedance (<math>\Omega</math>)</b>   | Impedance matched to Texas Instruments' CC1310, CC1312 chipsets  |                     |
| <b>Insertion Loss (dB)</b>                        | 1.3 typ. (1.6 max.)  | 1.8 typ. (2.2 max.) |
| <b>Return Loss (dB)</b>                           | 9.5 min.   | 9.5 min.            |
| <b>Phase Difference (deg)</b>                     | 180 $\pm$ 17   | 180 $\pm$ 15        |
| <b>Amplitude Difference (dB)</b>                  | 3.5 max.   | 2.0 max.            |
| <b>Attenuation (dB)</b>                           | 8 min.@ 1540 - 1720MHz<br>15 min.@ 1720 - 1736MHz<br>15 min.@ 1736 - 1856MHz<br>30 min.@ 2310 - 2580MHz<br>30 min.@ 2580 - 2784MHz<br>33 min.@ 3080 - 3440MHz<br>35 min.@ 3440 - 3712MHz |                     |
| <b>Power Capacity (W)</b>                         | 2 max. (CW)  |                     |



Texas Instruments - Approved!

|  |  |
|--|--|
| <b>Reel Quantity (pcs/reel)</b>  | 4,000  |
| <b>Operating Temperature</b>   | -40°C to +85°C                                     |
| <b>Recommended Storage Conditions and Period for unused Product on T&amp;R</b> | +5 to +35 °C<br>Humidity 45 - 75%RH<br>18 mos. max |

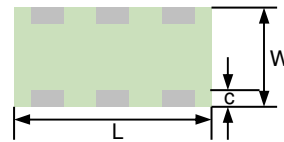
For more TI matched balun-filters, go to: <https://www.johansontechnology.com/ti>

## Part Number Explanation

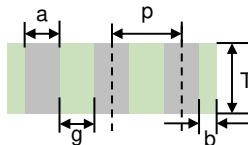
| P/N Suffix | Packaging Style | Bulk (loose pcs.) | Suffix = S    | eg. 0850BM14E0016S        |
|------------|-----------------|-------------------|---------------|---------------------------|
|            |                 | T & R             | Suffix = T    | eg. 0850BM14E0016T        |
|            |                 | 100% Tin          | Suffix = None | eg. 0850BM14E0016(T or S) |

## Mechanical Dimensions

|          | In                | mm              |
|----------|-------------------|-----------------|
| <b>L</b> | 0.063 $\pm$ 0.004 | 1.60 $\pm$ 0.10 |
| <b>W</b> | 0.031 $\pm$ 0.004 | 0.80 $\pm$ 0.10 |
| <b>T</b> | 0.024 $\pm$ 0.004 | 0.60 $\pm$ 0.10 |
| <b>a</b> | 0.008 $\pm$ 0.004 | 0.20 $\pm$ 0.10 |
| <b>b</b> | 0.008 +.004/-.008 | 0.20 +0.1/-0.2  |
| <b>c</b> | 0.006 $\pm$ 0.004 | 0.15 $\pm$ 0.10 |
| <b>g</b> | 0.012 $\pm$ 0.004 | 0.30 $\pm$ 0.10 |
| <b>p</b> | 0.020 $\pm$ 0.002 | 0.50 $\pm$ 0.05 |



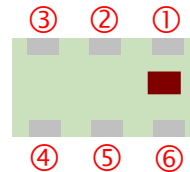
Bottom



Side

## Terminal Configuration

| No. | Function           |
|-----|--------------------|
| 1   | Unbalanced Port    |
| 2   | RX/TX              |
| 3   | Balanced Port RF_N |
| 4   | Balanced Port RF_P |
| 5   | GND                |
| 6   | GND                |



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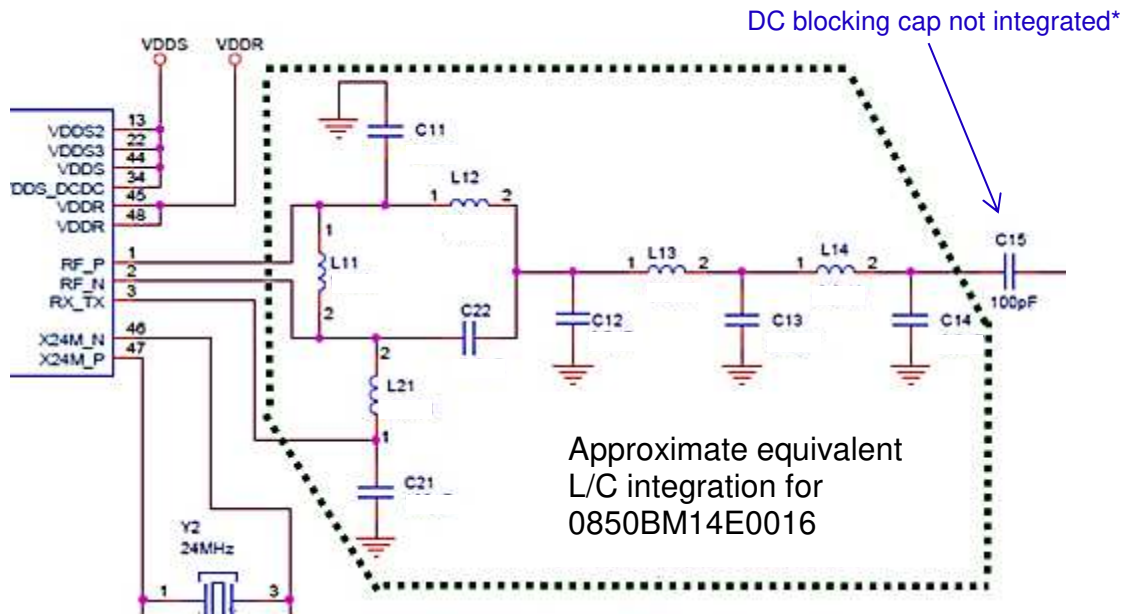
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## Equivalent L/C Circuit Integrated into IPC



\*Even though Pins 3, 4, and 1 are internally *DC blocked* from GND, Pins 3/4 are *DC coupled* to Pin 1 (there's a DC path between them). We recommend the designer place a DC blocking cap (68-100pF) in series after Pin 1 (between IPC and antenna).

Would you like the reference design, schematic, and gerber files? Need us to review your layout for free or an antenna recommendation for your application? Contact us at:

<https://www.johansontechnology.com/ask-a-question>

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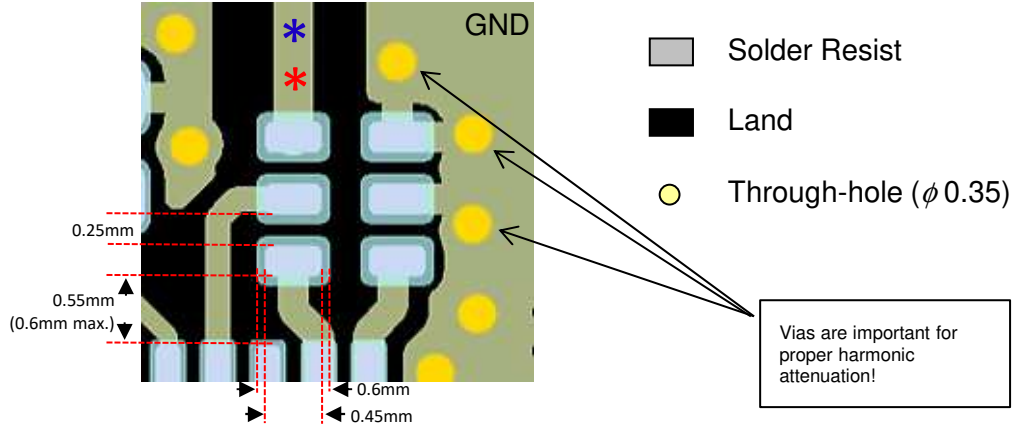
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## Pad-Soldermask Guidelines

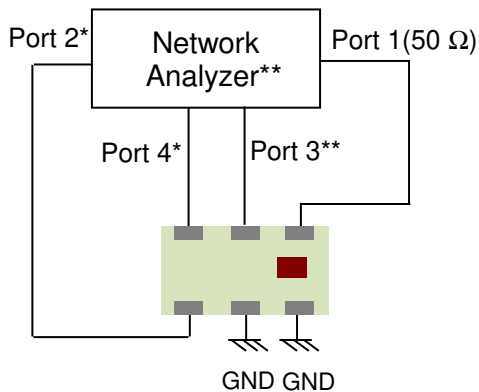
Request reference design, schematic, and gerber files at: <https://www.johansontechnology.com/ask-a-question>



\* Even though Pins 3, 4, and 1 are DC blocked from GND, Pins 3/4 are DC coupled to Pin 1 (there's a DC path between them). We recommend the designer place a DC blocking cap (68-100pF) in series after Pin 1 (between IPC and antenna) per page 2 of the datasheet.

\* Line width should be designed to match 50ohm characteristic impedance, depending on PCB material and thickness. Grounded CPWG is recommended.

## Measurement Diagram



Port 1: Unbalanced Port

Ports 2 and 4: Balanced Port

\*Ports 2 and 4 Terminate impedance

=Conjugate match to TI CC13XX chipset

Port 3: RX\_TX Port

\*\*Port 3 Terminate impedance

=The load Impedance looking into RX\_TX pin of TI CC13XX chipset

$IL = S_{DS21}$

$RL = S_{SS11}$

$Amp\_balance = dB(S(2,1)/S(4,1))$

$Phase\_balance = Phase(S(2,1)/S(4,1))$

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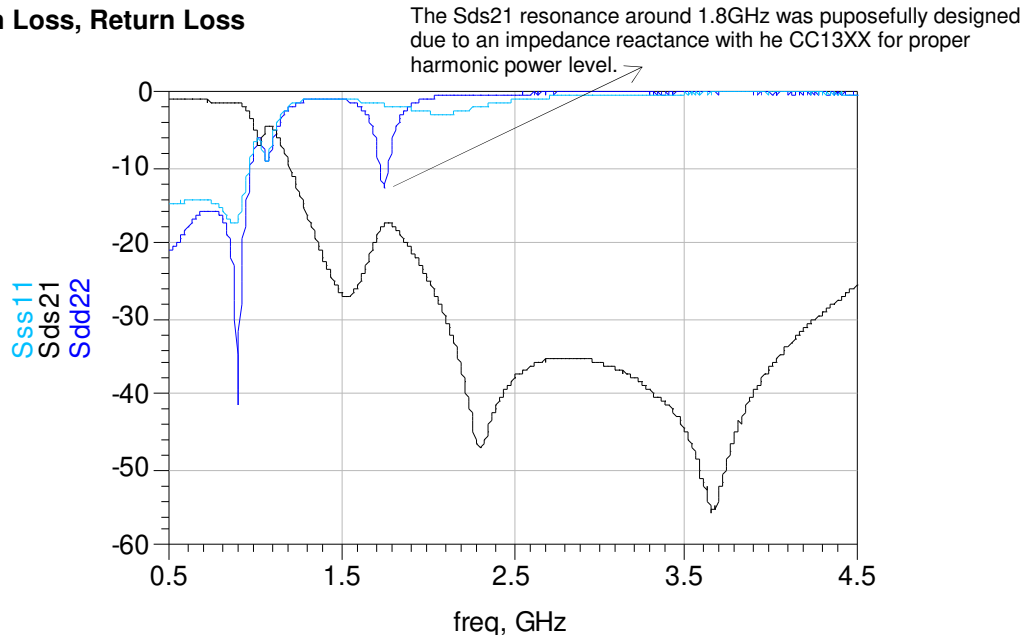
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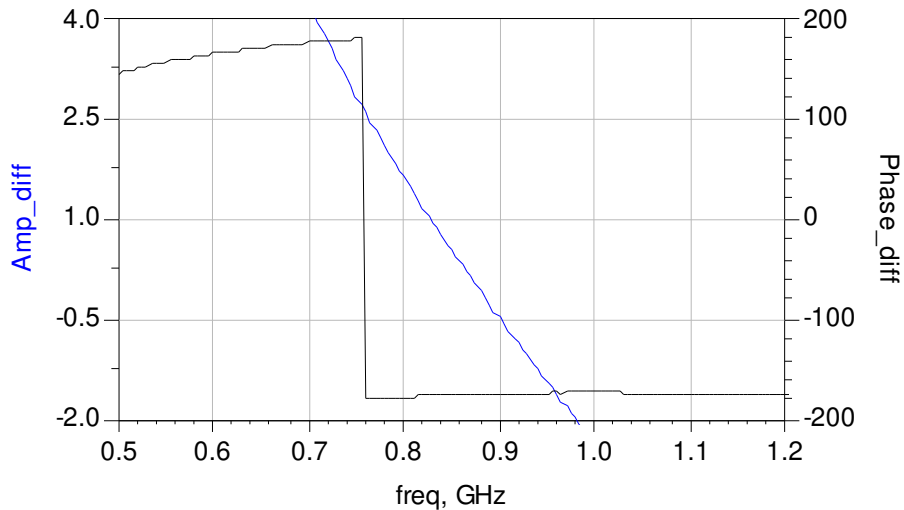
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## Typical Electrical Characteristics (T=25°C)

### Insertion Loss, Return Loss



### Phase Difference, Amplitude Balance



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## Application Notes, Layout Files, and more

<https://www.johansontechnology.com/ti>

## Small SMD 433MHz (or 900M, 2.4G, 5G) antennas

<https://www.johansontechnology.com/antennas>

## RoHS Compliance

<https://www.johansontechnology.com/rohs-compliance>

## Soldering Information

<https://www.johansontechnology.com/ipcsoldering-profile>

## Antenna layout and tuning techniques

<https://www.johansontechnology.com/tuning>

## Antenna layout review, tuning, and characterization services

<https://www.johansontechnology.com/ipc-antenna-services>

## MSL Info

<https://www.johansontechnology.com/msl-rating>

## Recommended Storage Condition and Max Shelf Life

<https://www.johansontechnology.com/recommended-storage-conditions>

## Packaging information

<https://www.johansontechnology.com/tape-reel-packaging>

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