

High Frequency Ceramic Solutions

AEC-Q200 Qualified Component

915MHz Impedance Matched/Balun/BPF Integrated Component for T.I. CC110X, CC111X, CC113X and CC115X, CC110L, CC113L, CC115L, CC430 and RF430

P/N 0915BM15A0001E-AEC

Detail Specification: 8/15/2017

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| General Specifications | | | |
|--------------------------|---|--|---|
| Part Number | 0915BM15A0001E-AEC | Insertion Loss | 2.0 dB max |
| Frequency (MHz) | 902 - 928 | Return Loss | 9.5 dB min. |
| Unbalanced Impedance | 50 Ω | Phase Difference | 180° ± 15 |
| Balanced Impedance | Impedance-Matched to T.I. CC110X, CC111X, CC113X and CC115X, CC110L, CC113L, CC115L, CC430 and RF430 Chipsets | Amplitude Difference | 1.5 dB |
| | | Input Power | 1W max. (CW) |
| | | Reel Quantity | 4,000 |
| Attenuation ¹ | 5 min. @ 745MHz | Operating Temperature | -40 to +105°C |
| | 30 min. @ 1830MHz | Recommended Storage Conditions for unused product on T&R | +5 ~ +35 °C, Humidity 45~75%RH, 18 months. 1 week max after opened ² |
| | 40 min. @ 2745MHz | | |
| | 45 min. @ 3660MHz | | |

¹Note: Band Pass Filtering included

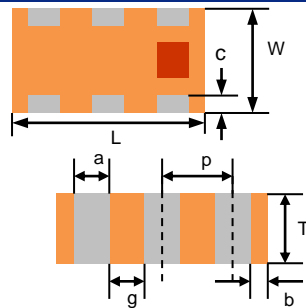
²For more info go to www.johansontechnology.com/silverlead

Part Number Explanation

| P/N Suffix | Packing Style | Bulk | Suffix = S | Eg. 0915BM15A0001S-AEC |
|------------|-------------------|---------------------|---------------|-------------------------------|
| | | T & R | Suffix = E | Eg. 0915BM15A0001E-AEC |
| | Termination style | AgPt | Suffix = None | Eg. 0915BM15A0001(E or S)-AEC |
| | Evaluation Board | 0915BM15A0001-EBSMA | | |

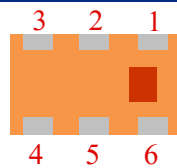
Mechanical Dimensions

| | In | mm |
|---|--------------------|----------------|
| L | 0.079 ± 0.004 | 2.00 ± 0.10 |
| W | 0.049 ± 0.004 | 1.25 ± 0.10 |
| T | 0.028 ± 0.004 | 0.70 ± 0.10 |
| a | 0.012 ± 0.004 | 0.30 ± 0.10 |
| b | 0.008 ± 0.004 | 0.20 ± 0.10 |
| c | 0.012 +.004/-0.008 | 0.30 +0.1/-0.2 |
| g | 0.014 ± 0.004 | 0.35 ± 0.10 |
| p | 0.026 ± 0.002 | 0.65 ± 0.05 |



Terminal Configuration

| No. | Function | No. | Function |
|-----|----------------------------|-----|----------------------------|
| 1 | Unbalanced Port | 4 | Balanced Port ³ |
| 2 | GND | 5 | GND |
| 3 | Balanced Port ³ | 6 | GND |



³Balanced ports are DC-Blocked from pins 1-2-5-6. Blocking capacitor is embedded, no need for external DC-Blocking cap at GND pins or unbalanced

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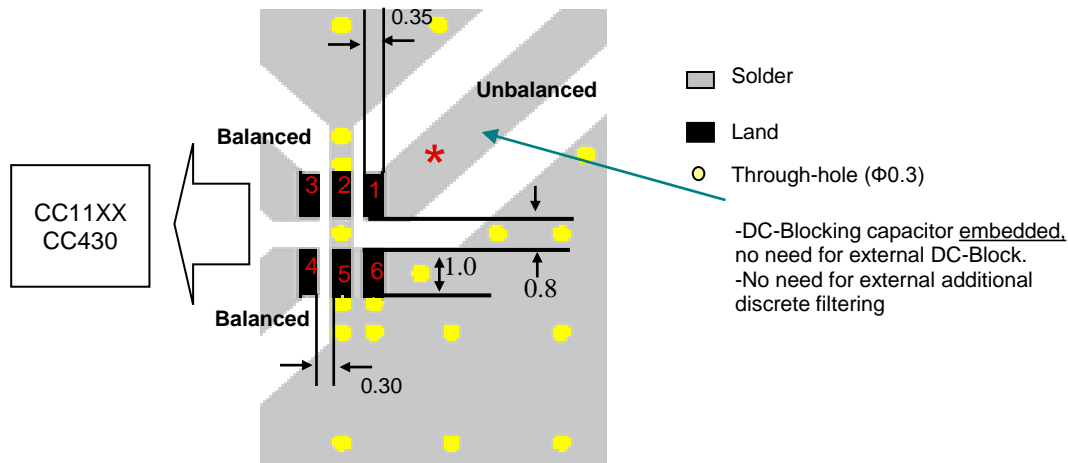
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Mounting Considerations

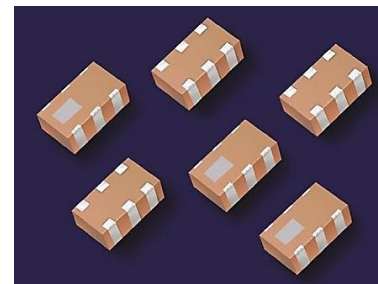
Mount these devices with brown mark facing up. Units: mm

* Line width should be designed to provide 50 Ω impedance matching characteristics.

Pin assignment reference



To obtain application notes, information how to implement this component, or obtain gerber files, go to: www.johansontechnology.com/ti or contact our Apps Engineering Team at: www.johansontechnology.com/component/techquestion/?Itemid=407



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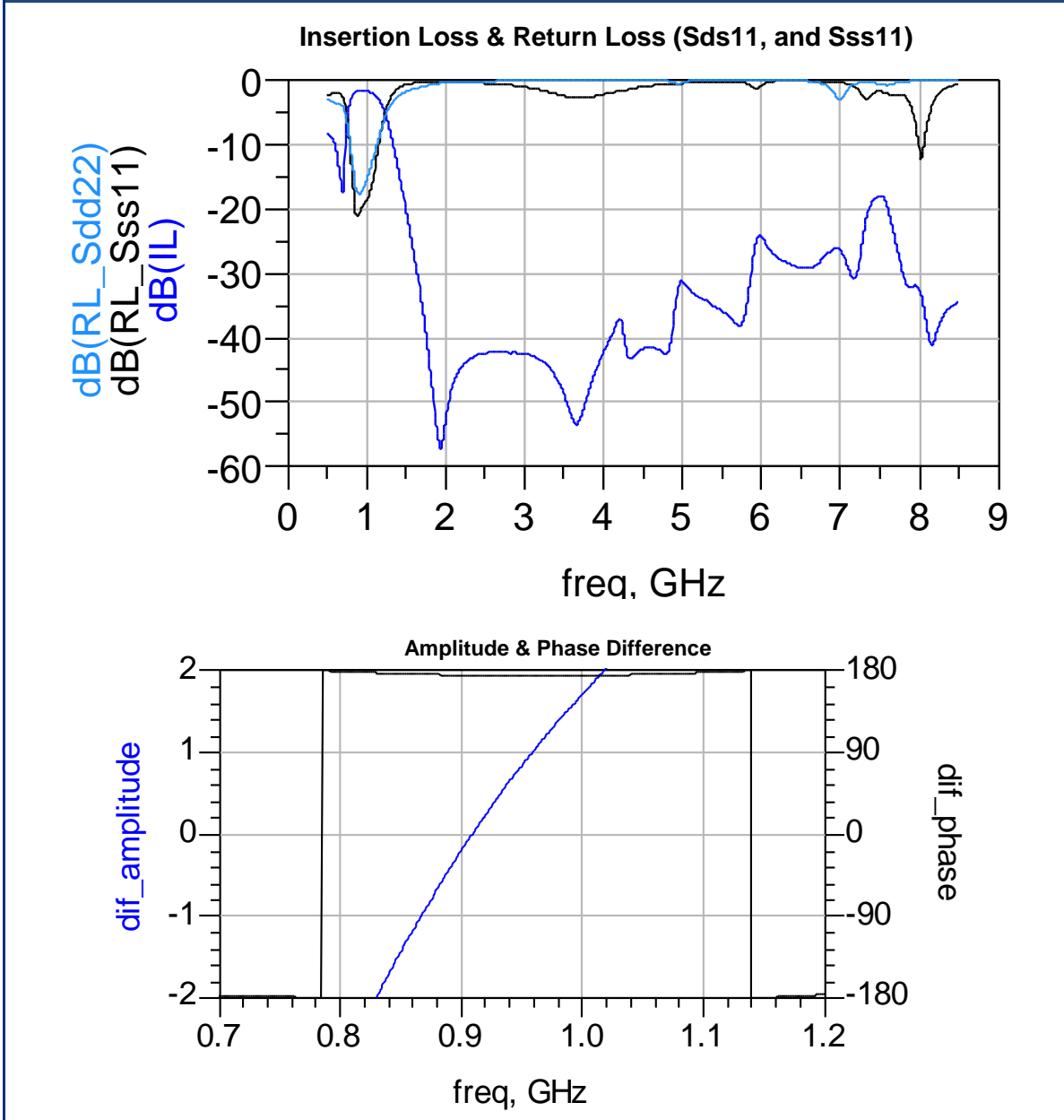
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Typical Electrical Performance (T=25°C)



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